

In re Patent Application of:  
**D'ALBORE ET AL.**  
Serial No. **10/820,462**  
Filed: **APRIL 8, 2004**

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**In the Specification:**

Please replace paragraph 17 with the following  
rewritten paragraph:

With specific reference to the view of Figure 2, an electronic embedded system, for instance a CPU based system, is schematically shown at 1. According to the invention, the system 1 includes at least a first read only memory (ROM) portion 2, a second extended memory portion 3, and at least an additional memory portion 5. The first memory portion 2 may be an electrically erasable memory portion of the EEPROM or Flash type.